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			Application Number	09/595776	
Sheet 1 of 3			Filing Date	06/16/00	
			First Named Inventor	ENRIQUE MUSOLL	
			Group Art Unit	2183	
			Examiner Name	DAVID J. HUISMAN	
			Attorney Docket Number	MIPS.0166-00-US	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2, (if known)</sup>			
DJH		US-4200927	04/29/1980	Hughes et al.	
DJH		US-6535905	03/18/2003	Kalafatis et al.	
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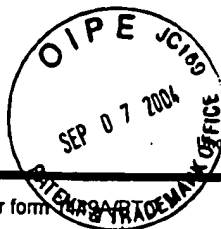
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5 (if known)</sup>				

Examiner Signature	<i>David J. Huisman</i>	Date Considered	10-7-04
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Sheet	2	of	3

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issuue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DJH	AA	YAMAMOTO, WAYNE. <i>An Analysis of Multistreamed, Superscalar Processor Architectures</i> . University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
DJH	AB	YAMAMOTO et al. "Increasing Superscalar Performance Through Multistreaming." <i>Parallel Architectures and Compilation Techniques (PACT '95)</i> . 1995	
DJH	AC	<i>The PowerPC Architecture: A Specification for a New Family of RISC Processors</i> . 2 <sup>nd</sup> Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
DJH	AD	<i>MC68020 32-Bit Microprocessor User's Manual</i> . 3 <sup>rd</sup> Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
DJH	AE	POTEL, M. J. "Real-Time Playback in Animation Systems." <i>Proceedings of the 4<sup>th</sup> Annual Conference on Computer Graphics and Interactive Techniques</i> . 1977. pp. 72-77. San Jose, CA, US.	
DJH	AF	<i>ARM Architecture Reference Manual</i> . 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
DJH	AG	<i>ESA/390 Principles of Operation</i> . IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
DJH	AH	<i>MC88110 Second Generation RISC Microprocessor User's Manual</i> . 1991. pp. 10-66, 10-67, and 10-71. Motorola, Inc.	
DJH	AI	DIEFENDORFF, KEITH ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." <i>IEEE Journal of Microelectronics</i> . April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	
DJH	AJ	KANE, GERRY. <i>PA-RISC 2.0 Architecture</i> . 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
DJH	AK	DIEFENDORFF, KEITH ET AL. "Altivec Extension to PowerPC Accelerates Media Processing." <i>IEEE Journal of Microelectronics</i> . Vol. 20, No. 2 (2000): pp. 85-95	
DJH	AL	GRUNEWALD, WINFRIED ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." <i>Proceedings of EUROMICRO 22</i> , 1996. pp. 592-599	
DJH	AM	BRADFORD, JEFFREY ET AL. "Efficient Synchronization for Multithreaded Processors." <i>Workshop on Multithreaded Execution, Architecture, and Compilation</i> . Jan-Feb 1998. pp. 1-4	
DJH	AN	PAI, VIJAY ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." <i>Proceedings of ASPLOS-VII</i> , Oct. 1996: pp. 12-23, ACM, Inc.	

Examiner Signature	<i>David J. Huisman</i>	Date Considered	11-7-04
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